

FIG. 1

PRIOR ART

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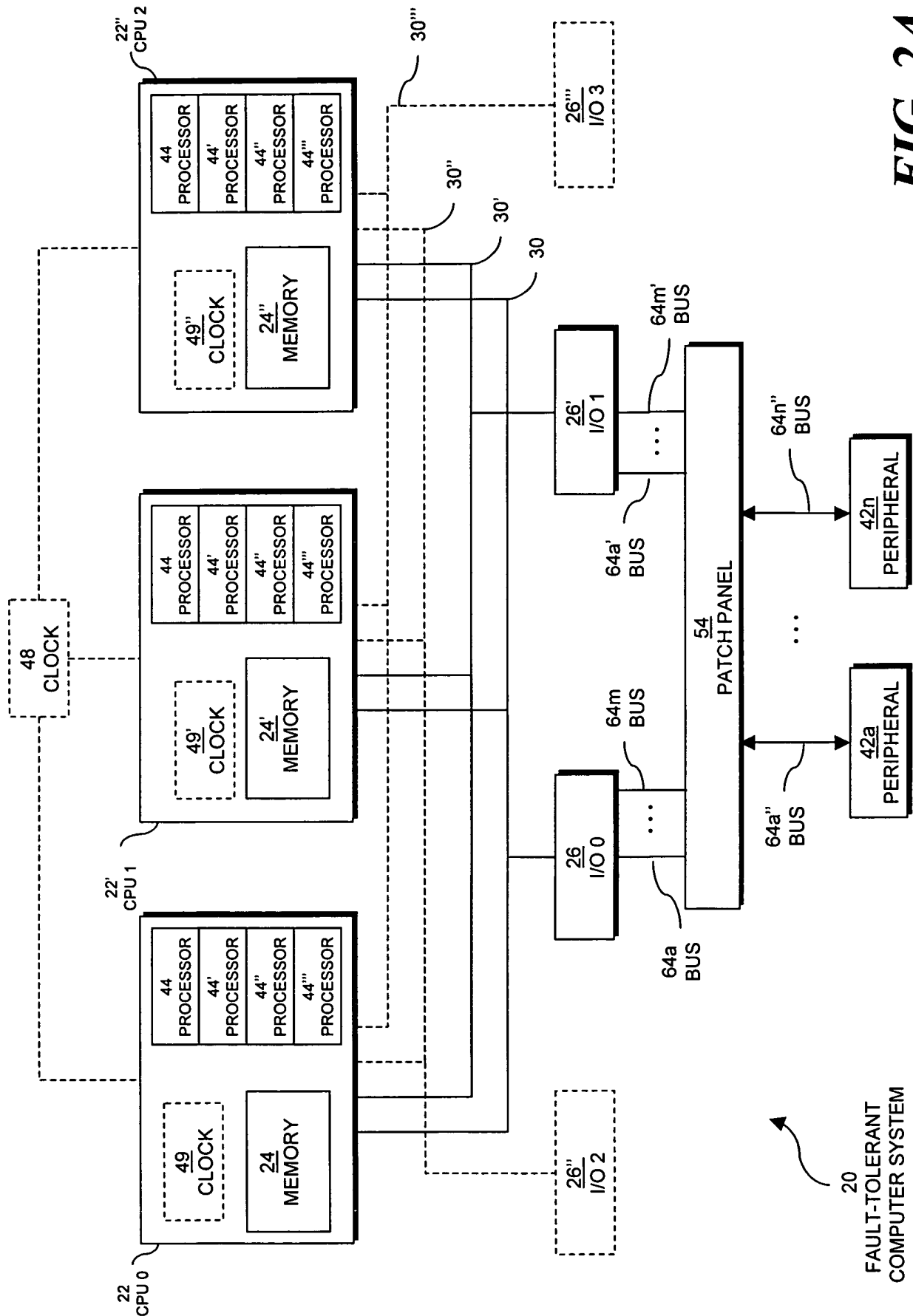
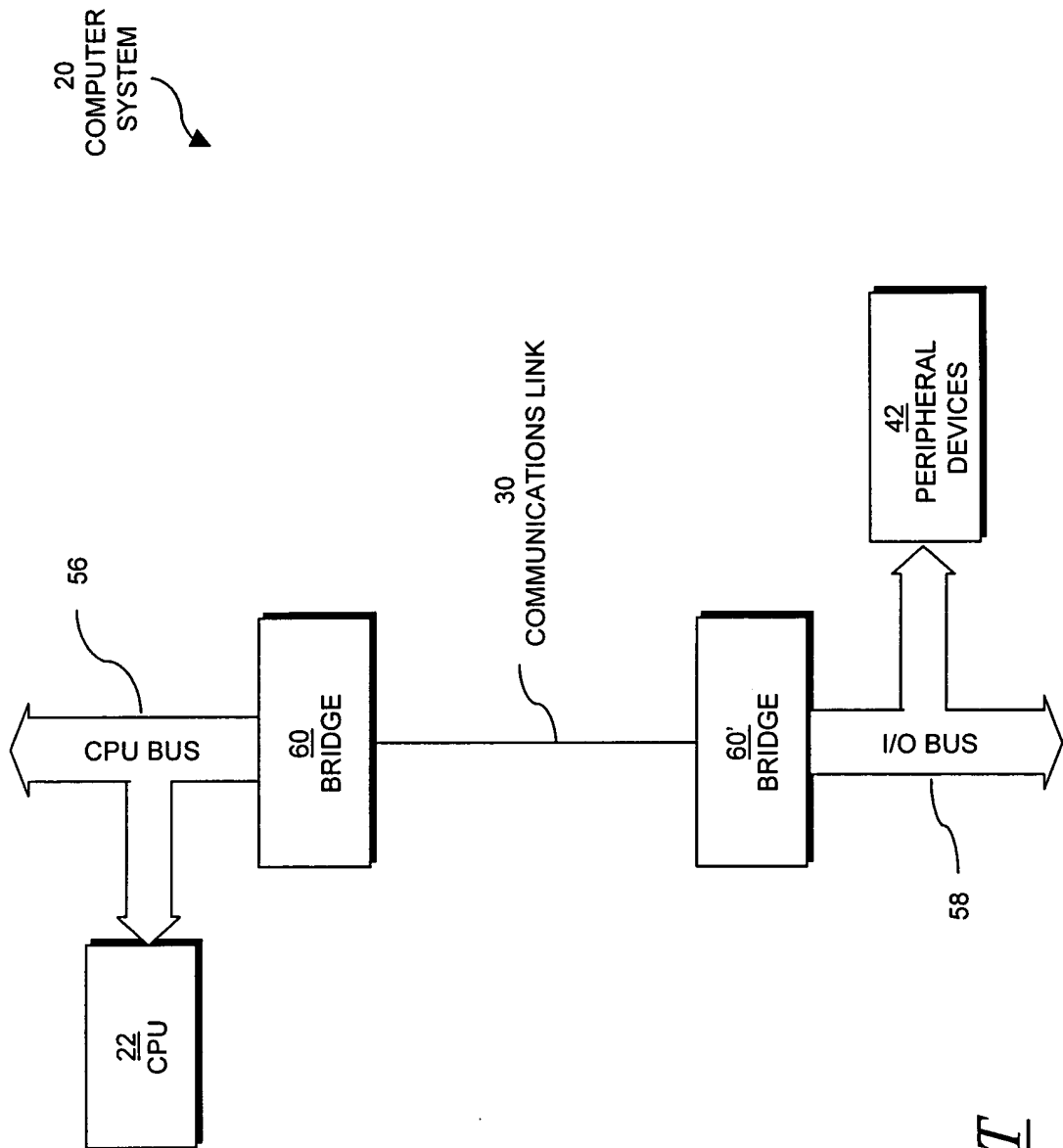


FIG. 2A



PRIOR ART

FIG. 2B

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 FAULT-TOLERANT
 COMPUTER SYSTEM

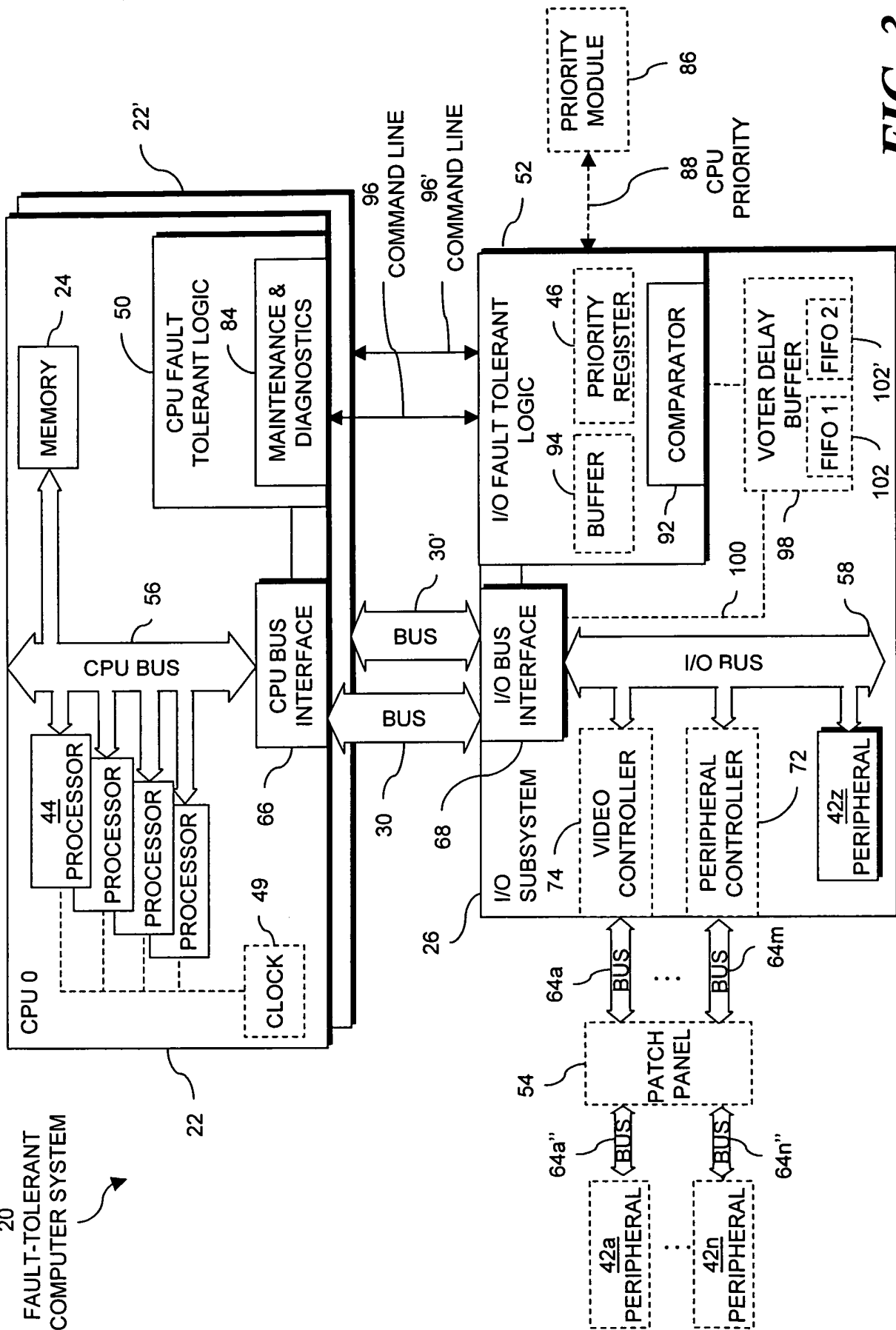


FIG. 3

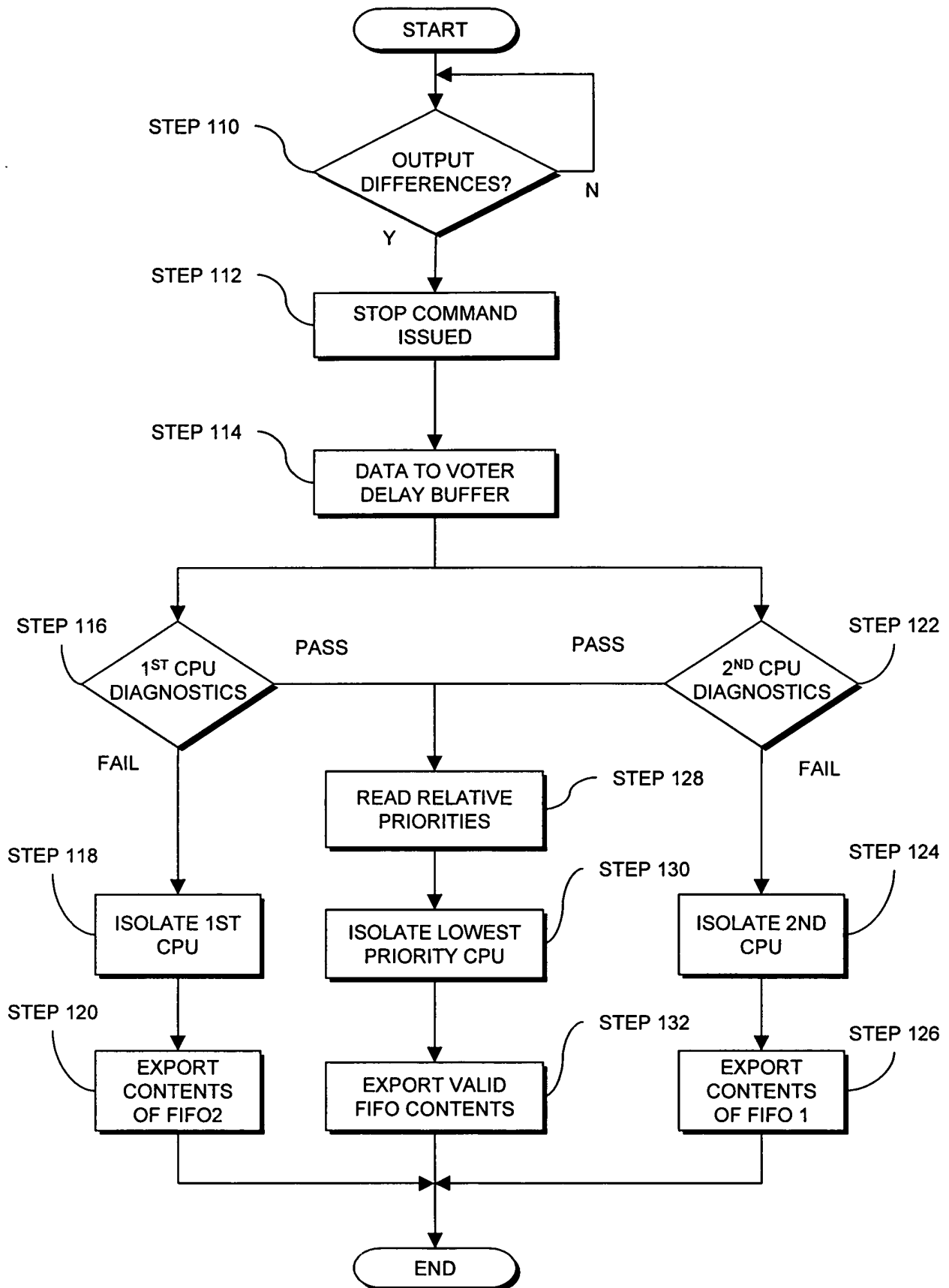


FIG. 4

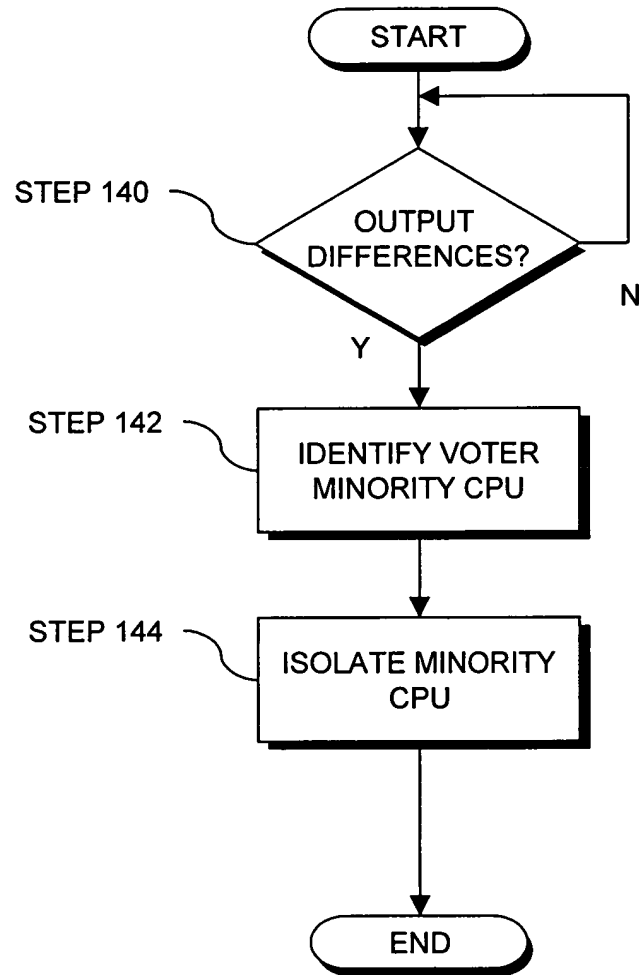


FIG. 5

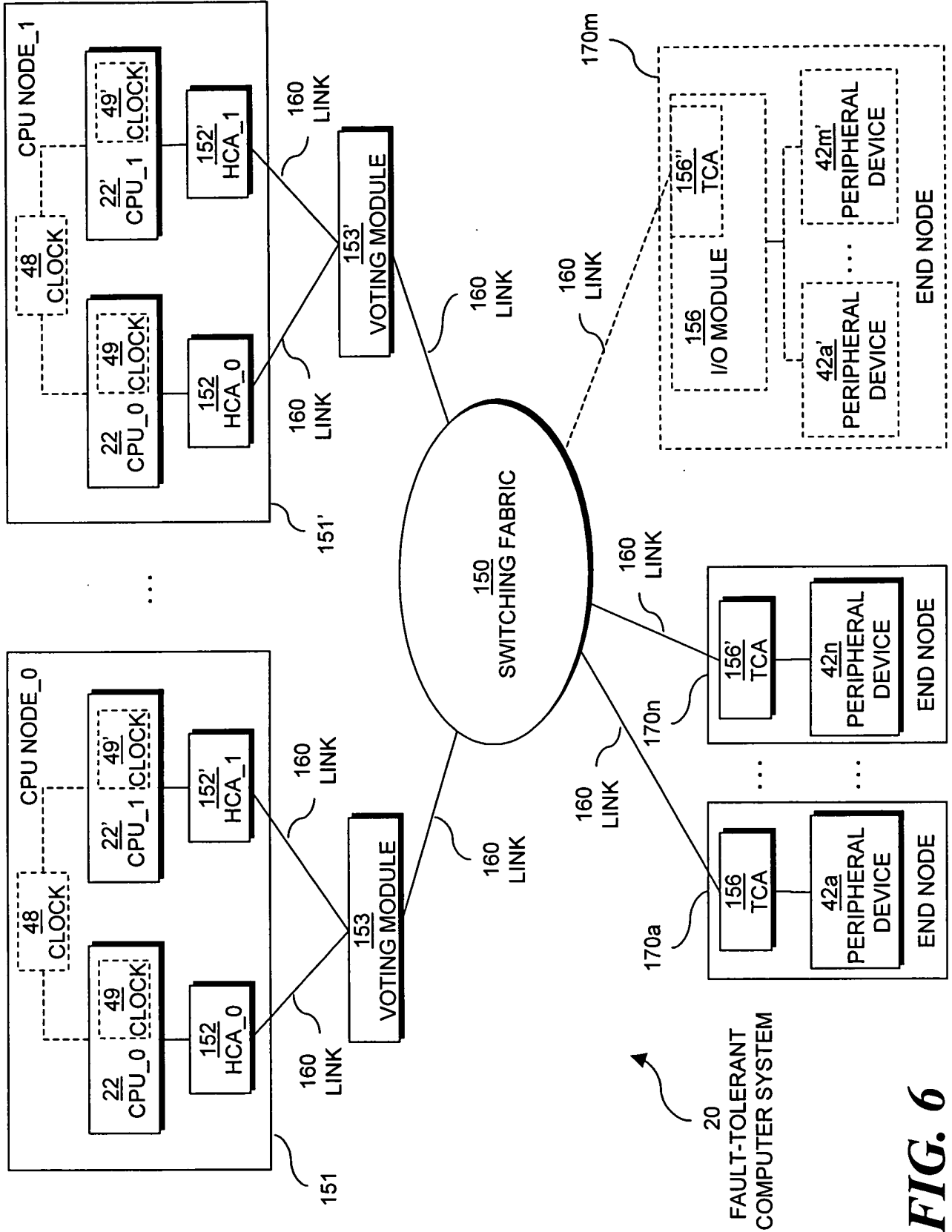


FIG. 6

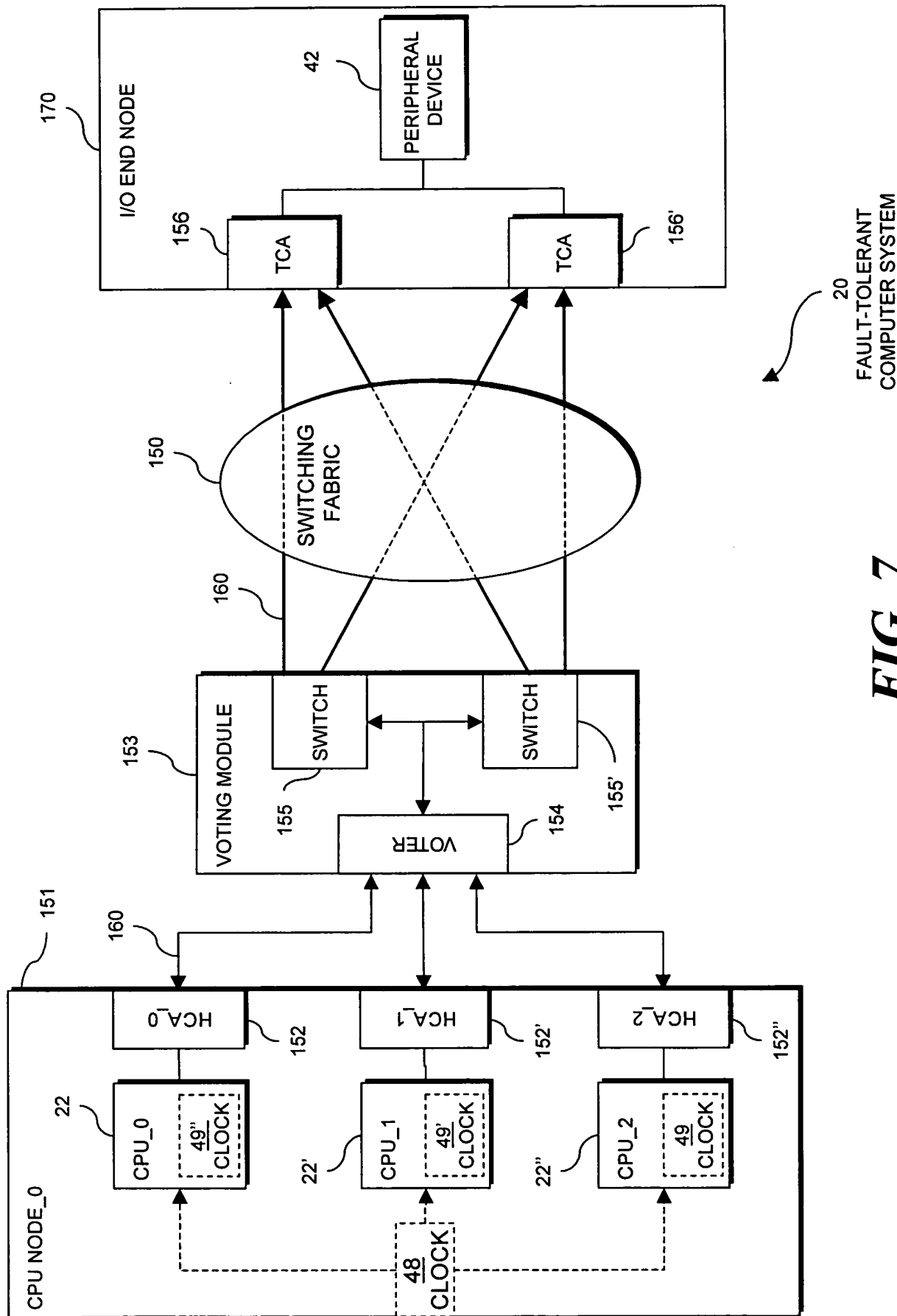


FIG. 7